

Abridged Translation of Cited Reference

Reference 2:

Publication No.: JP-B-8-33851 (corresponding to JP62214449)

Date of Publication: September 21, 1987

Application No.: 57775/'86

Date of Application: March 15, 1986

Convention Priority: None

Applicant: HITACHI LTD

Inventors: TANAKA TOSHIHARU, et al.

Title of the Invention: MONITORING SYSTEM FOR VIRTUAL COMPUTER

(Lines 7-11 of 9th column):

If the value of the running mode display 14 is "0" and the status bit 11-2-k of corresponding counter register 10-2-k is "1", the counter registers 10-1-k and 10-2-k of the hardware counter group 9-2 for VM is being updated. If the other case, both of the counter registers 10-1-k and 10-2-k are not updated.

(Lines 40-44 of 13th column):

If the channel monitoring enable bit 34-j is "1", an area that the value of the MBI 30-k is added to the contents of the MBO register 32-j is accessed and updated, while the storage protection key is the measurement blocking key register 37-j.